3LCD MODULE

SPECIFICATION

Model:	UE050WV-RB40-A091A
Version:	V1.1
Date:	20210429

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Customer Confirmation 客户确认

Approved by	Notes

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VIEWE Confirmation 优奕确认

Prepared by	Reviewed by	Approved by

REVISION HISTORY

Revision 版本号	Date 日期	Contents of Revision Change 修改内容	Remark 备注
V1.0	20210323	Preliminary release	All
V1.1	20210429	修改 TP 电压为 3.3V	

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TABLE of CONTENTS

1. GENERAL INFORMATION..... 4

1.1 Features..... 4

1.2 Mechanical Specification..... 4

2. ABSOLUTE MAXIMUM RATINGS..... 5

3. MECHANICAL DRAWING..... 6

4. I/O CONNECTION & BLOCK DIAGRAM..... 7

4.1 I/O Connection..... 7

4.2 Block Diagram..... 8

5. ELECTRICAL CHARACTERISTICS..... 9

5.1 TFT-LCD Panel Driving Section..... 9

5.2 Back Light Driving Section..... 9

5.3 Power On/Off Sequence..... 10

5.3.1 Power on sequence..... 10

5.3.2 Power off sequence..... 10

TP..... 11

5.4 Timing Characteristics..... 12

5.4.0 TP Timing..... 12

5.4.1 System Operation AC Characteristics..... 12

5.4.2 Parallel 24-bit RGB Input Timing Table..... 13

5.5 Timing Diagram..... 13

6. OPTICAL CHARACTERISTICS..... 14

7. RELIABILITY..... 17

8. PACKAGE DRAWING..... 18

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1. GENERAL INFORMATION

1.1 Features

1) Pixel Arrangement：RGB Vertical Stripe
2) Interface Mode：40PIN_RGB 24bits
3) Driver IC：ST7262-G4-1-E；TP IC：FT6336U
4) Operation Temperature：-20~70℃
5) Storage Temperature：-30~80℃
6) Backlight Type：White LED
7) Display mode：Normally black
8) Pixel Density：186 PPI
9) LED life time：30,000 Hours

1.2 Mechanical Specification

Item 项目	Specification 规格	Unit 单位	Remark 备注
Pixel Driving element	TFT	-	-
Screen Size	5.0 IPS	Inch	Diagonal
Resolution	800(W)*3(RGB)*480(H)	Dots	-
Interface	RGB 24bits	-	40PIN
Module Power Consumption	1.148	Watt	Typ.
Active Area	108(W)*64.80(H)	mm	-
Pixel pitch (W*H)	45(W)*135(H)	um	-
Module Size (W*H*D)	120.7(W)*75.8(H)*4.2(D)	mm	-
Luminance	340	cd/m^2	Typ.
Viewing Direction	All	O'clock	
Display Color	16.7M	Colors	24bits

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2. ABSOLUTE MAXIMUM RATINGS

Item 项目	Symbol 符号	Min. 最小值	Max. 最大值	Unit 单位	Remark 备注
Power supply1 voltage	VDD	-0.3	4	V	Note1
Power supply2 voltage	VCC	-0.3	3.6	V	Note1
LED forward current	I_F	-0.001	30	mA	For each led,Note1
LED Reverse Voltage	V_R	-	5	V	For each led,Note1
Operating temperature	T_{op}	-20	70	℃	Note1,2
Storage temperature	T_{st}	-30	80	℃	Note1,2
Humidity	H_{st}	10	90	%RH	Note1,3

(Ta=+25°C,DGND=AVSS=0V)

Note1:If the module exceeds the absolute maximum ratings, it may be damaged permanently. Also if the module operates with the absolute maximum ratings for a long time, the reliability may drop.

Note2: In case of temperature below 0℃,the response time of liquid crystal (LC) becomes slower and the color of panel darker than normal one.

Note3: Temp. ≤ 60℃ , 90% RH MAX.
Temp. ＞60℃ , Absolute humidity shall be less than 90% RH.

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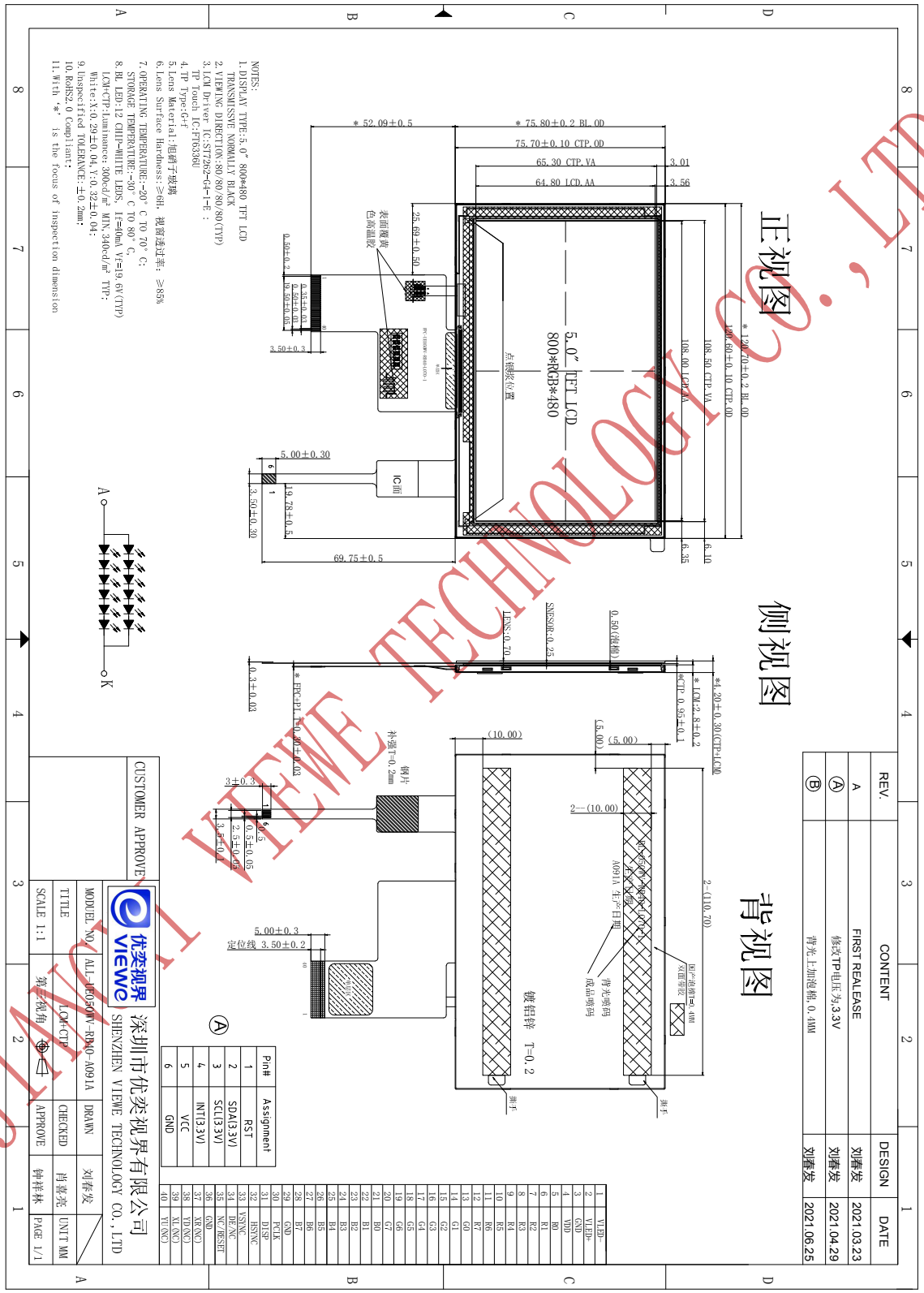
3. MECHANICAL DRAWING



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4. I/O CONNECTION & BLOCK DIAGRAM

4.1 I/O Connection

Pin No.	Symbol 符号	I/O	Description 描述
1	VLED-	P	Power supply for backlight cathode
2	VLED+	P	Power supply for backlight anode
3	GND	P	Power Ground
4	VDD	P	Power supply to the internal logic power regulator(3. 3V)
5-12	R0-R7	I	Red data input
13-20	G0-G7	I	Green data input
21-28	B0-B7	I	Blue data input
29	GND	P	Power Ground
30	PCLK	I	Pixel clock input pin, Negative polarity
31	DISP	I	Standby mode. Normally pulled high
32	HSYNC	I	Horizontal sync signal, Negative polarity
33	VSYNC	I	Vertical sync signal, Negative polarity
34	DE	I	Data input enable. Display access is enabled when DE is “H”
35	NC/RESET	-	Dummy
36	GND	P	Power Ground
37	XR(NC)	-	Dummy
38	YD(NC)	-	Dummy
39	XL(NC)	-	Dummy
40	YU(NC)	-	Dummy

TP

Pin No.	Symbol 符号	I/O	Description 描述
1	RST	I	The signal will reset the TP,Signal is active low
2	SDA(3.3V)	I/O	I2C data signal for TP
3	SCL(3.3V)	I	I2C clock signals for TP
4	INT(3.3V)	O	Interrupt signals for TP
5	VCC	P	Power supply for TP
6	GND	P	Power Ground

I：Input；O：Output；P：Power

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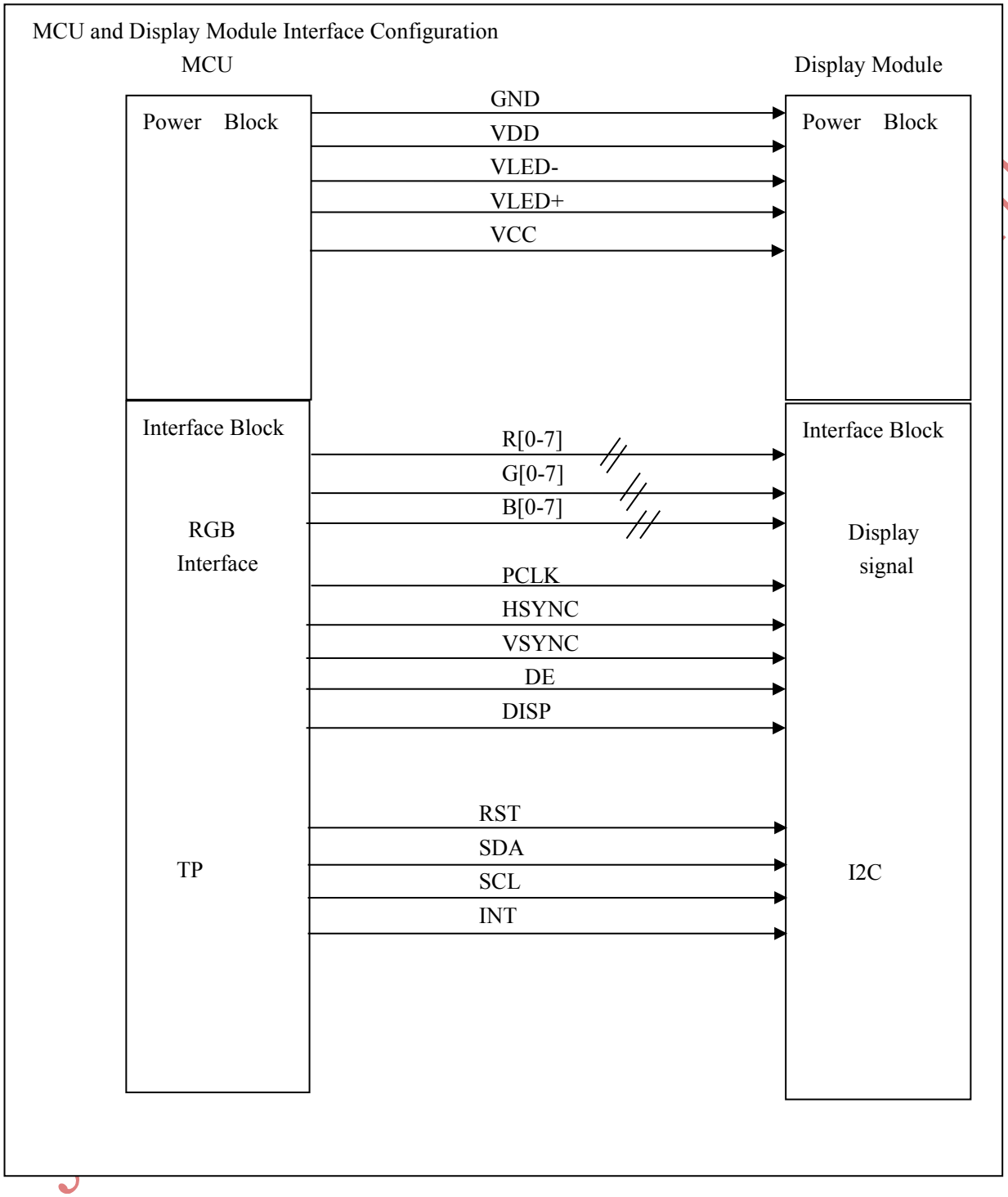
4.2 Block Diagram



5. ELECTRICAL CHARACTERISTICS

5.1 TFT-LCD Panel Driving Section

Item 项目	Symbol 符号	Min. 最小值	Typ. 典型值	Max. 最大值	Unit 单位	Remark 备注
Power Supply1 Voltage	VDD	2.8	3.3	3.6	V	-
Power Supply2 Voltage	VCC	-	3.3	-	V	
Power Supply1 Current	I_{VDD}	-	55	-	mA	Note1
Power Supply2 Current	I_{VCC}	-	50	-	mA	Note1
Logic Input High Voltage	V_{IH}	0.7VDD	-	VDD	V	-
Logic Input Low Voltage	V_{IL}	0	-	0.3VDD	V	-
Panel Power Consumption	P_D	-	0.346	-	Watt	Note1
Module Power Consumption	P_A	-	1.148	-	Watt	Note1,2

(Ta=+25°C, DGND=AVSS=0V)

Note1:Measurement Conditions (Video Mode): Full Screen Red Pattern,VDD=3.3V,60Hz Refresh.

Note2: $P_A = P_D + P_{BL}$， About P_{BL} information, inference to 5.2 Back Light Driving Section.

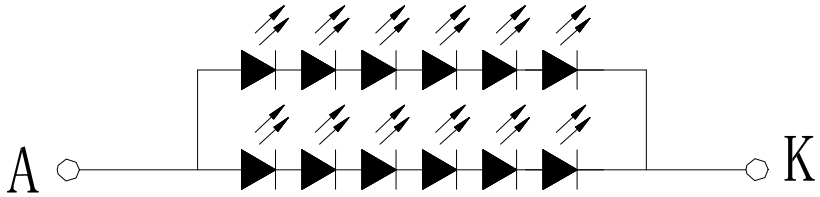
5.2 Back Light Driving Section

Item 项目	Symbol 符号	Min. 最小值	Typ. 典型值	Max. 最大值	Unit 单位	Remark 备注
Forward Voltage	V_F	-	19.6	-	V	Note1
Forward Current	I_F	-	40	-	mA	Note1
Backlight Power consumption	P_{BL}	-	0.784	-	Watt	Note1
LED life time	-	30000	-	-	Hrs	Note2
LED Quantity			12		PCS	

(Ta=+25°C, DGND=AVSS=0V)

Note1: The LED driving condition is defined for each LED module (6LED Serial, 2 LED Parallel)。
For each LED ： IF=20mA,VF=3.2V(Typ.)/3.4V(Max.),Ta=25℃。

Note2:The “LED life time” is defined as the module brightness decrease to 50% of original brightness at I_{LED}=20mA(Per Led). The LED life time could be decreased if operating I_{LED} is larger than 20mA.



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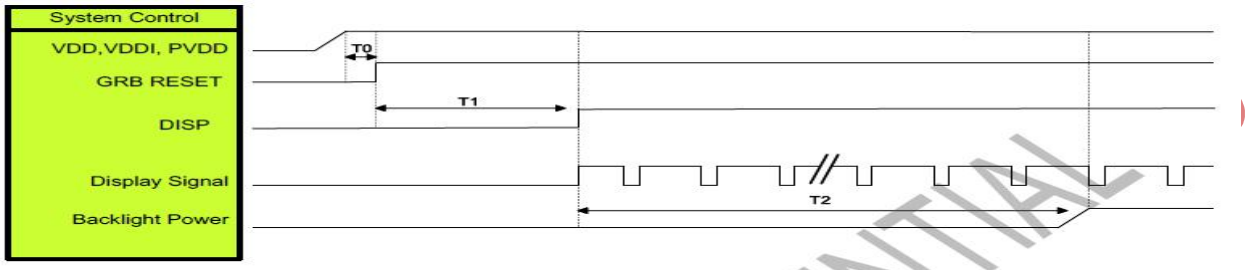
5.3 Power On/Off Sequence

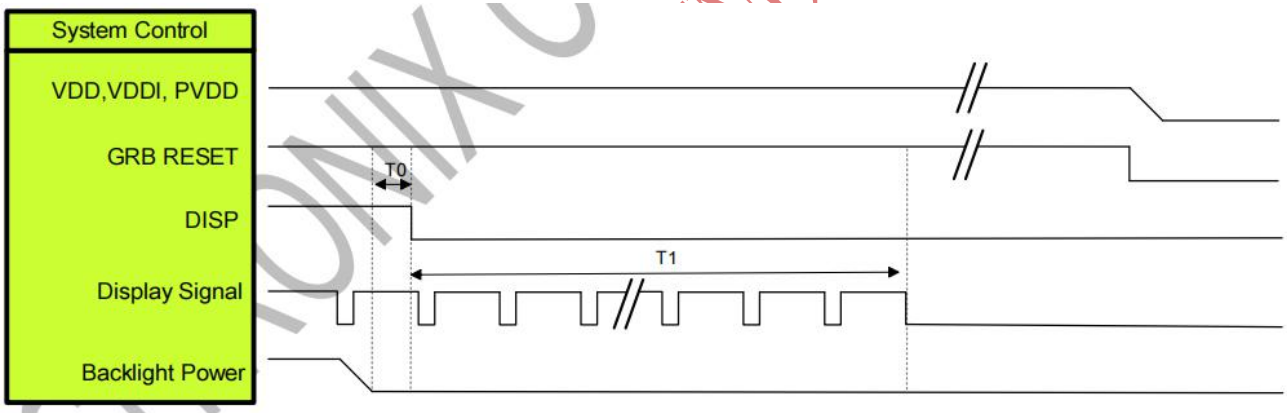
5.3.1 Power on sequence



Symbol	Description	Min. Time	Unit
T0	System power stability to GRB RESET signal	0	ms
T1	GRB RESET= "High" to DISP="High"	10	ms
T2	Display Signal output to Backlight Power on	250	ms

Note: Display signal: DCLK; VSYNC; HSYNC; DE; DR[7:0]; DG[7:0]; DB[7:0]

5.3.2 Power off sequence



Symbol	Description	Min. Time	Unit
T0	Backlight Power off to DISP="Low"	5	ms
T1	DISP="Low" to IC internal voltage discharge complete	100	ms

Note: RGB interface Display signal: DCLK; VSYNC; HSYNC; DE; DR[7:0]; DG[7:0]; DB[7:0]

Note: LVDS interface Display signal: DCLK P/N; RX[3:0]P/N

TP

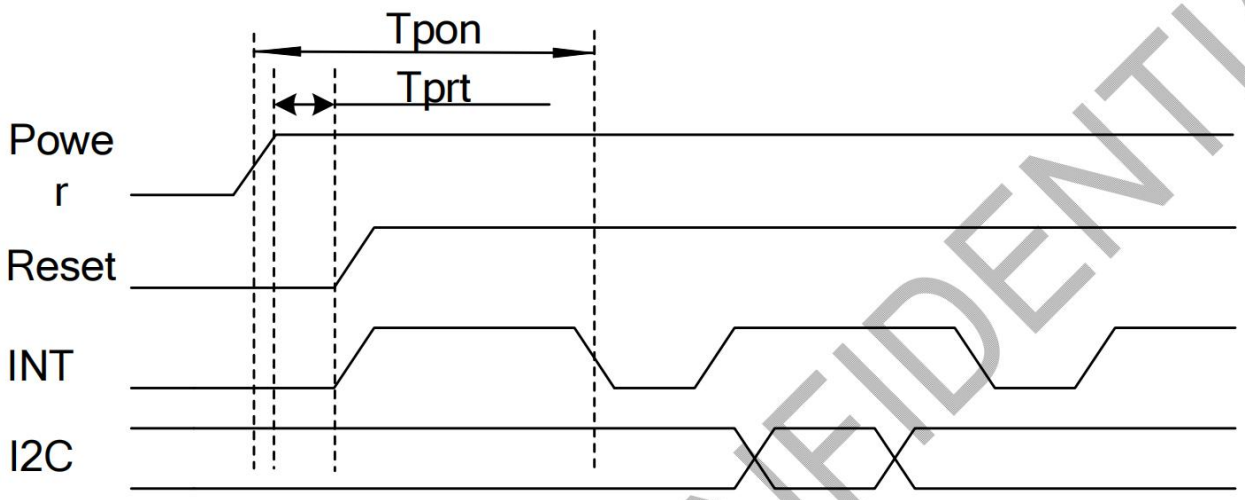


Figure 3-9 Power on Sequence

Reset time must be enough to guarantee reliable reset, the time of starting to report point after resetting approach to the time of starting to report point after powering on.

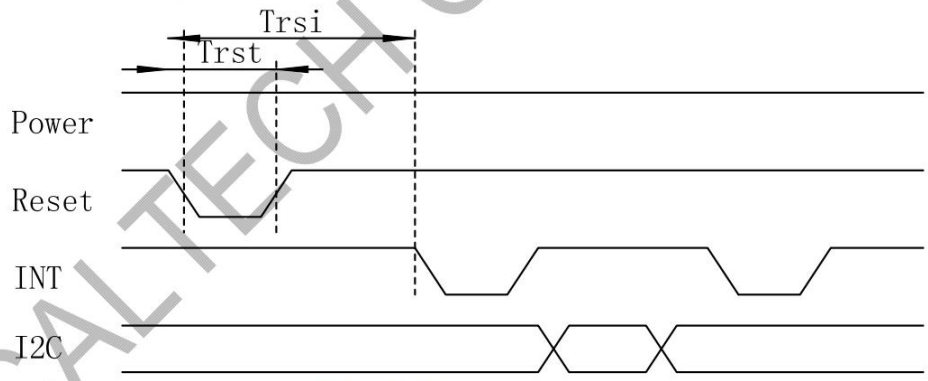


Figure 3-10 Reset Sequence

Table 3-5 Power on/Reset/Wake Sequence Parameters

Parameter	Description	Min	Max	Units
Tris	Rise time from 0.1VDD to 0.9VDD	-	3	ms
Tpon	Time of starting to report point after powering on	300	-	ms
Tprt	Time of being low after powering on	1	-	ms
Trsi	Time of starting to report point after resetting	300	-	ms
Trst	Reset time	5	-	ms

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5.4 Timing Characteristics

5.4.0 TP Timing

Table 2-2 I2C Timing Characteristics

Parameter	Min	Max	Unit
SCL frequency	10	400	KHz
Bus free time between a STOP and START condition	4.7	\	us
Hold time (repeated) START condition	4.0	\	us
Data setup time	250	\	ns
Setup time for a repeated START condition	4.7	\	us
Setup Time for STOP condition	4.0	\	us

5.4.1 System Operation AC Characteristics

Item	Symbol	Min.	Typ.	Max.	Unit	Conditions
VDD Power Source Slew Time	TPOR	-	-	20	ms	From 0V to 99% VDD
GRB Pulse Width	tRSTW	10	50	-	us	R=10Kohm, C=1uF
SD Output Stable Time	Tst	-	-	TBD	us	Output settled within +20mV Loading = 6.8k+28.2pF.
GD Output Rise and Fall Time	Tgst	-	-	TBD	us	Output settled (5%~95%), Loading = 4.7k+29.8pF

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5.4.2 Parallel 24-bit RGB Input Timing Table

Parallel 24-bit RGB Interface Timing Table							
Item		Symbol	Min.	Typ.	Max.	Unit	Remark
DCLK Frequency		Fclk	23	25	27	MHz	
HSYNC	Period Time	Th	808	816	896	DCLK	
	Display Period	Thdisp		800		DCLK	
	Back Porch	Thbp	4	8	48	DCLK	
	Front Porch	Thfp	4	8	48	DCLK	
	Pulse Width	Thw	2	4	8	DCLK	
VSYNC	Period Time	Tv	488	496	504	HSYNC	
	Display Period	Tvdisp		480		HSYNC	
	Back Porch	Tvbp	4	8	12	HSYNC	
	Front Porch	Tvfp	4	8	12	HSYNC	
	Pulse Width	Tvw	2	4	8	HSYNC	

RGB Mode Selection Table	DCLK	HSYNC	VSYNC	DE
SYNC - DE Mode	Input	Input	Input	Input
SYNC Mode	Input	Input	Input	GND
DE Mode	Input	GND	GND	Input

Note: "Input" means these signals are driven by host side

5.5 Timing Diagram

6. OPTICAL CHARACTERISTICS

Parameter 参数	Symbol 符号	Condition 条件	Min. 最小值	Typ. 典型值	Max. 最大值	Unit 单位	Remark 备注
Contrast Ratio	C/R	θ = 0°	1000	1500	-	-	Note(4)
NTSC Ratio	S	θ =0°	55	60	-	%	Note(7)
Luminance	L	θ =0°	300	340	-	cd/m2	Note(5)
Luminance uniformity	U_W	θ =0°	70	80	-	%	Note(3)
Response Time	T_R+ T_F	25 °C	-	30	40	ms	Note(2)
Color Coordination	W_X	θ = 0° (Center) Normal viewing angle B/L On	-0.04	0.29	+0.04	NTSC (x,y)	Note(6)
	W_Y			0.32			
	R_X			0.638			
	R_Y			0.338			
	G_X			0.296			
	G_y			0.575			
	B_X			0.137			
	B_Y			0.124			
Viewing Angle	θ_L	C/R>10	70	80	-	Degree	Note(1)
	θ_R		70	80	-		
	θ_U		70	80	-		
	θ_D		70	80	-		

Test Conditions:

1. VDD=3.3V, I_F=60mA (Backlight current), the ambient temperature is+25℃.

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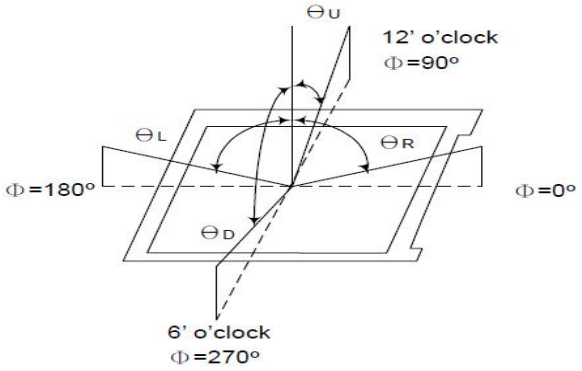
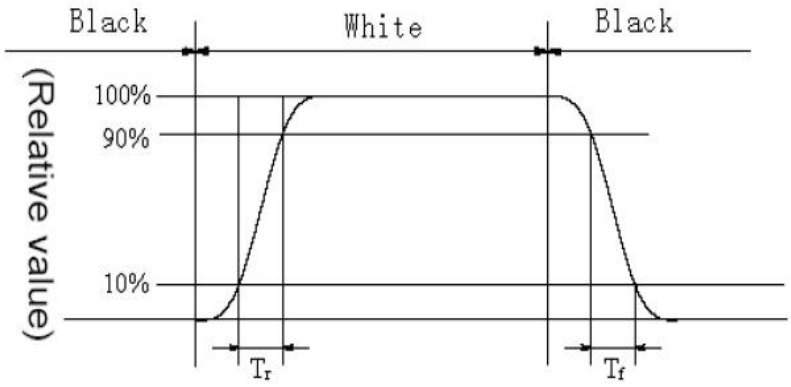
2. The test systems refer to Note 8.

Note1: Definition of Viewing Angle: The viewing angle range that the CR>10

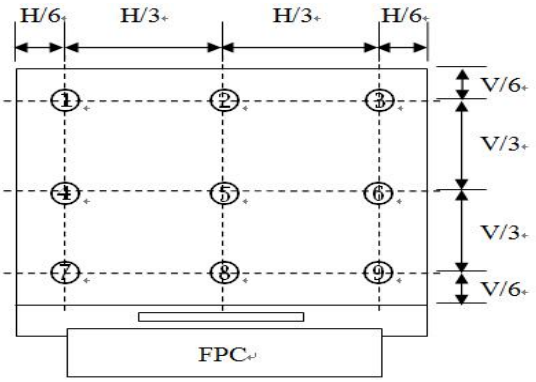


Note2: Definition of Response time: Sum of T_R and T_F



Note 3: Definition of Luminance Uniformity: Active area is divided into 9 measuring areas, every measuring point is placed at the center of each measuring area.

$$\text{Luminance Uniformity} = \frac{\text{Min Luminance of white among 9-points}}{\text{Max Luminance of white among 9-points}} \times 100\%$$



Note4: Definition of Contrast Ratio (CR): measured at the center point of panel

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$$

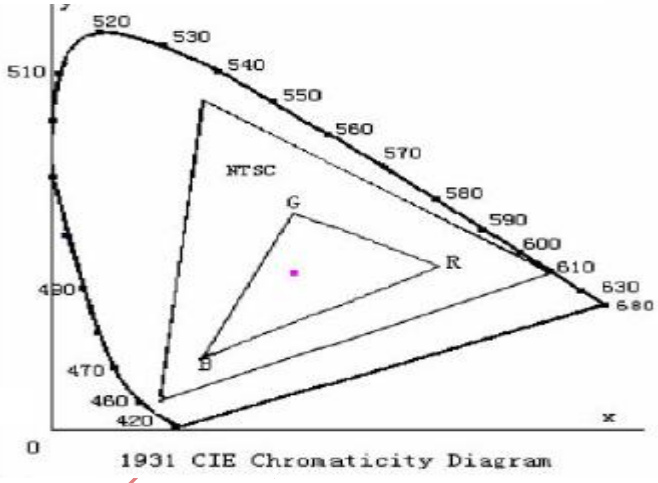
Note 5: Definition of Luminance: Center Luminance of white is defined as luminance values of 1point average across the LCD surface.

Note 6: Definition of Color Chromaticity (CIE 1931)

Color coordinates of white & red, green, blue measured at center point of LCD.

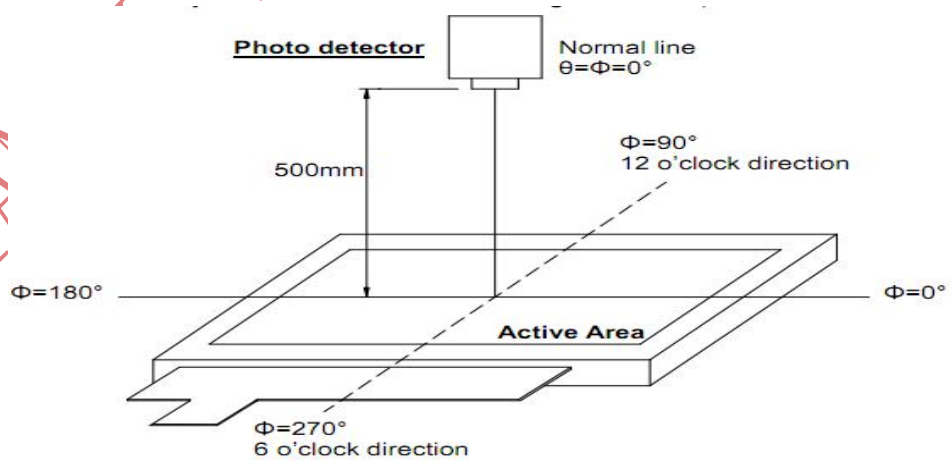
Note 7: Definition of NTSC ratio:

$$\text{NTSC ratio} = \frac{\text{Area of RGB triangle}}{\text{Area of NTSC triangle}}$$



Note 8: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 5 minutes operation, the optical properties are measured at the center point of the LCD screen.(Response time is measured by Photo detector TOPCON BM-7, Field of view: 1°/Height: 500mm.)



7. RELIABILITY

Item 项目	Test Condition 测试条件	Remark 备注
High Temperature Storage	Ta =+80°C / 96Hours	Note1,2,3
Low Temperature Storage	Ta =-30°C / 96Hours	Note1,2,3
High Temperature Operating	Ta =+70°C / 96Hours	Note1,2,3
Low Temperature Operating	Ta =-20°C / 96Hours	Note1,2,3
Temperature Cycle storage Test	-40°C/30min ⇆+90°C /30min for 30cycles,Transfer time less than 5min	Note2,3
Thermal humidity storage Test	60°C x 90%RH / 96Hours	Note2,3
Package Vibration Test	Frequency: 10Hz~55Hz,Amplitude:1.5mm, 1 hrs for each direction of X, Y, Z	Note2
ESD	C=150PF,R=330 Ohm Air:±8kv,5times(Center) Contact: ±4kv,5times(Center)	Note4

Inspection after Test:

Note1:Ta is the ambient temperature of samples.

Note 2: In the standard condition, there shall be no practical problem that may affect the display function. After the reliability test, the product only guarantees operation, but doesn't guarantee all the cosmetic specification.

Note 3: Before cosmetic and function tests , the product must have enough recovery time, at least 2 hours at room temperature.

Note 4: In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judged as a good part.

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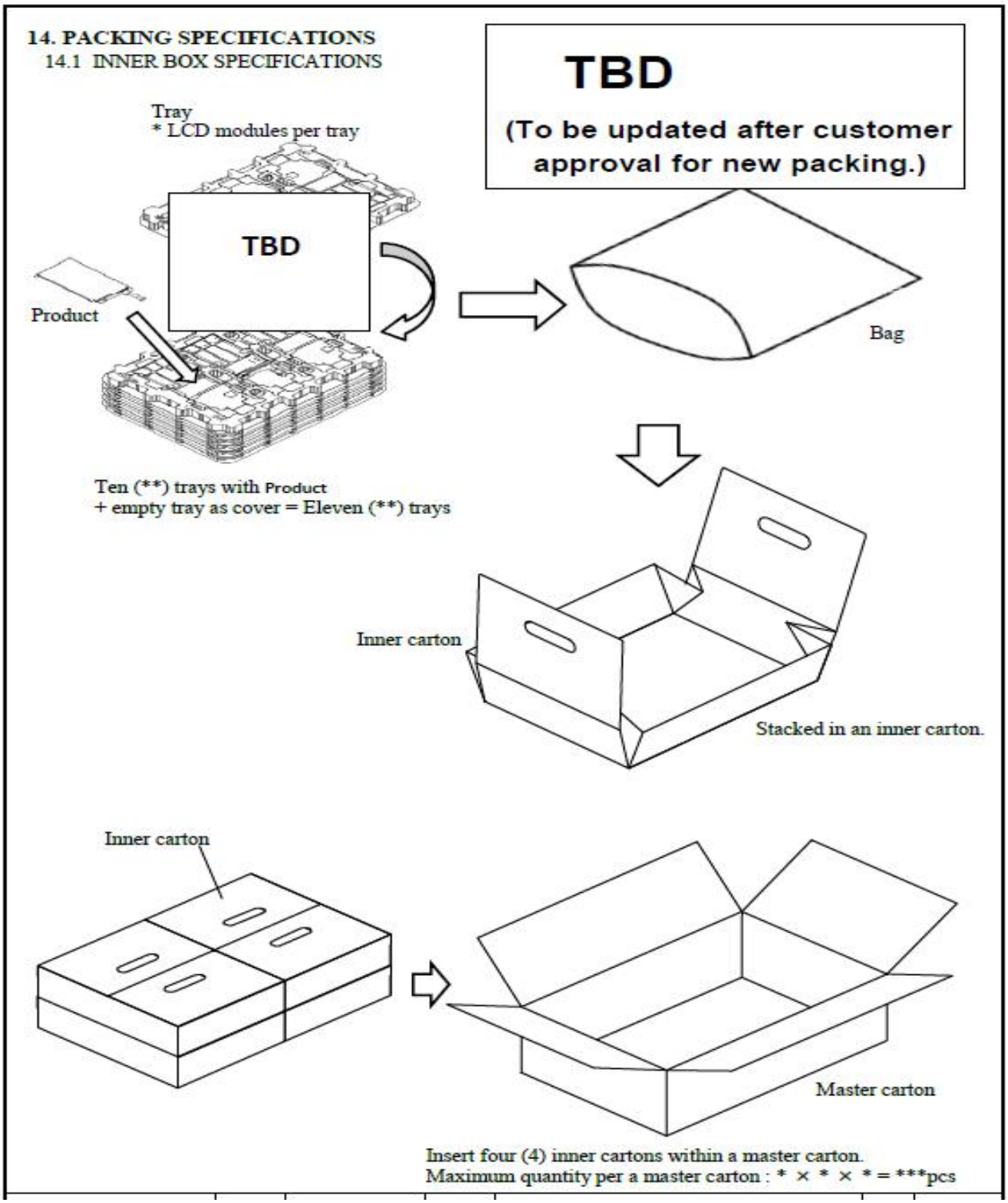
8. PACKAGE DRAWING